ABSTRACT OF THE DISCLOSURE

The present invention relates to a circuit and method for symbol timing recovery in phase modulation systems. First, phase differences of in-phase signals and quadrature signals in a polar coordinate are generated at the same sampling points of neighboring symbols. Second, an operator is generated by taking a square of a value subtracting the phase difference from a default phase value. Third, the sums of phase differences of every sampling point in a symbol are computed to determine an optimal sampling point in a symbol period and correctly recover symbol timing of signal sequences. By the above properties, the present invention has a simple structure and can shorten an execution time.